### SN54145, SN54LS145, SN74145, SN74LS145 BCD-TO-DECIMAL DECODERS/DRIVERS

SDLS051

#### MARCH 1974 - REVISED MARCH 1988

### FOR USE AS LAMP, RELAY, OR MOS DRIVERS

- Full Decoding of Input Logic
- SN54145, SN74145, and SN74LS145 Have 80-mA Sink-Current Capability
- All Outputs Are Off for Invalid BCD Input Conditions
- Low Power Dissipation of 'LS145 ...
  35 mW Typical

| FUNCTION | TABLE |
|----------|-------|
|          |       |

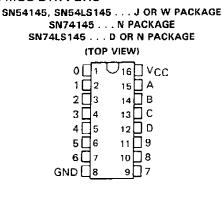
| r       |    |     |     |   |   |   |   |   |    |     |   |   |   |   |
|---------|----|-----|-----|---|---|---|---|---|----|-----|---|---|---|---|
| NO.     |    | INP | UTS |   |   |   |   | 0 | UT | PUT | S |   |   |   |
| 1.0.    | D  | C   | 8   | Α | 0 | 1 | 2 | 3 | 4  | 5   | 6 | 7 | 8 | 9 |
| 0       | L  | L   | L   | L | L | н | н | н | Н  | Н   | Н | Н | Н | Н |
| 1       | L  | L   | L   | н | H | L | н | н | н  | Н   | н | н | н | н |
| 2       | L. | L.  | н   | L | н | н | L | н | н  | н   | н | н | н | н |
| 3       | L  | L   | н   | н | н | н | н | L | н  | Н   | н | н | н | н |
| 4       | Ł  | н   | L   | L | н | н | н | Н | L  | Н   | Н | Н | н | Н |
| 5       | Ł  | н   | L   | Н | н | н | н | н | н  | L   | н | н | н | Ξ |
| 6       | L  | н   | н   | L | н | н | н | н | н  | н   | Ł | н | н | н |
| 7       | L  | н   | н   | н | н | н | н | н | н  | н   | н | L | Н | н |
| 8       | н  | L   | L   | L | н | н | н | н | н  | н   | н | н | L | н |
| 9       | н  | L   | L   | н | н | н | н | н | н  | н   | н | н | н | L |
|         | Н  | ٦L  | Н   | L | н | H | Н | Н | Н  | Н   | Н | Н | Н | Ŧ |
|         | н  | L   | н   | н | н | н | н | н | н  | н   | н | Н | н | н |
| Ē       | н  | н   | L   | L | н | н | н | н | н  | н   | н | н | н | н |
| INVALID | н  | н   | L   | н | н | н | н | н | н  | н   | н | н | н | н |
| =       | н  | Н   | н   | L | н | н | н | н | н  | н   | н | н | н | н |
|         | н  | н   | н   | н | н | н | н | н | н  | н   | н | н | н | н |

H = high level (off), L = low level (on)

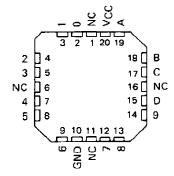
#### description

These monolithic BCD-to-decimal decoder/drivers consist of eight inverters and ten four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid BCD input logic ensures that all outputs remain off for all invalid binary input conditions. These decoders feature high-performance, n-p-n output transistors designed for use as indicator/relay drivers or as open-collector logic-circuit drivers. Each of the highbreakdown output transistors (15 volts) of the SN54145, SN74145, or SN74LS145 will sink up to 80 milliamperes of current. Each input is one Series 54/74 or Series 54LS/74LS standard load, respectively. Inputs and outputs are entirely compatible for use with TTL or DTL logic circuits, and the outputs are compatible for interfacing with most MOS integrated circuits. Power dissipation is typically 215 milliwatts for the '145 and 35 milliwatts for the 'LS145.

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

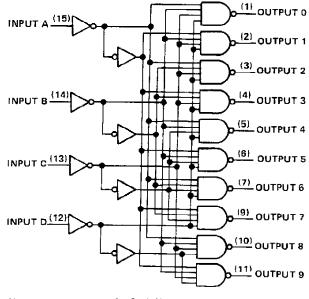






NC - No internal connection





Pin numbers shown are for D, J, N. and W packages.



## SN54LS145, SN74LS145 **BCD-TO-DECIMAL DECODERS/DRIVERS**

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

| Supply voltage, V <sub>CC</sub> (see Note 1)                 |
|--|
| Input voltage , , , , , , , , , , , , , , , , , , ,          |
| Maximum current into any output (off-state)                  |
| Operating free-air temperature range: SN54145                |
| SN74145  |
| Storage temperature range $-65^{\circ}$ C to $150^{\circ}$ C |

NOTE 1: Voltage values are with respect to network ground terminal.

### recommended operating conditions

|                                    |     | SN5414 | 5   | SN74145 |     |      |     |
|------------------------------------|-----|--------|-----|---------|-----|------|-----|
|                                    | MIN | NOM    | MAX | MIN     | NOM | MAX  |     |
| Supply voltage, VCC                | 4.5 | 5      | 5.5 | 4.75    | 5   | 5.25 | v   |
| Off-state output voltage, VO(off)  |     |        | 15  |         |     | 15   | V   |
| Operating free-air temperature, TA | -55 |        | 125 | 0       |     | 70   | ° C |

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

|                 | PARAMETER                              | TEST CONDIT  | IONST                      | MIN | TYPİ | ΜΑΧ        | UNIT |
|-----------------|--|--|----------------------------|-----|------|------------|------|
| VIH             | High-level input voltage               |  |                            | 2   |      |            | V    |
| V <sub>IL</sub> | Low-level input voltage                |  |                            |     |      | 0.8        | V    |
| Vik             | Input clamp voltage                    | V <sub>CC</sub> = MIN, I <sub>l</sub> = -12 mA                           |                            |     |      | -1.5       | V    |
| IO(off)         | Off-state output current               | $V_{CC} = MIN, V_{IH} = 2 V,$<br>$V_{IL} = 0.8 V, V_{O(off)} = 15$       | v                          |     |      | 250        | μA   |
| VOion)          | On-state output voltage                | V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V,<br>V <sub>IL</sub> = 0.8 V | 1 <sub>O(on)</sub> = 80 mA |     | 0.5  | 0.9<br>0.4 | v    |
| 1               | Input current at maximum input voltage | VCC = MAX, VI = 5.5 V  |                            | -   |      | 1          | mA   |
| Η               | High-level input current               | V <sub>CC</sub> = MAX, V <sub>1</sub> - 2.4 V                            |                            |     |      | 40         | μA   |
| <u>۱</u> ι      | Low-level input current                | V <sub>CC</sub> = MAX, V <sub>1</sub> = 0.4 V                            | ····                       |     |      | -1.6       | mA   |
| 1               | Supply current                         | Mar MAY Cashier 7  | SN54145                    |     | 43   | 62         |      |
| lcc             | Supply current                         | V <sub>CC</sub> = MAX, See Note 2  | SN74145                    |     | 43   | 70         | mΑ   |

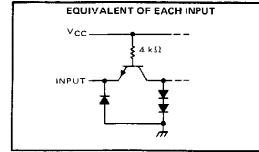
<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. <sup>‡</sup>All typical values are at  $V_{CC} = 5 V$ ,  $T_A = 25$ °C. NOTE 2: I<sub>CC</sub> is measured with all inputs grounded and outputs open.

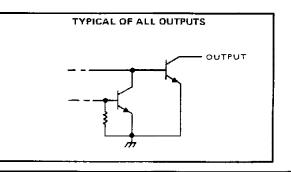
## switching characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = $25^{\circ}$ C

|                  | PARAMETER  |                      | TEST CONDITI            | MIN         | MAX | UNIT |    |
|------------------|--|----------------------|-------------------------|-------------|-----|------|----|
| <b>TPLH</b>      | Propagation delay time, low-to-high-level output | $C_{1} = 15  pF_{2}$ | R <sub>1</sub> = 100 Ω. | See Note 3  |     | 50   | ns |
| <sup>t</sup> PHL | Propagation delay time, high-to-low-level output | ν <u>Γ</u> - 15 μr,  | н <u>Г</u> - 10032,     | See 1001e S |     | 50   | ns |

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

### schematics of inputs and outputs





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### SN54145, SN74145 **BCD-TO-DECIMAL DECODERS/DRIVERS**

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

| Supply voltage, V <sub>CC</sub> (see Note 1)                  |  |  |  |   |  |  |   |  |  |     |     |    |       |
|---|--|--|--|---|--|--|---|--|--|-----|-----|----|-------|
| Input voltage<br>Operating free-air temperature range: SN54LS |  |  |  |   |  |  |   |  |  |     |     |    |       |
| SN74LS  |  |  |  |   |  |  |   |  |  |     |     |    |       |
| Storage temperature range                                     |  |  |  | - |  |  | - |  |  | -65 | 5°C | to | 150°C |

NOTE 1: Voltage values are with respect to network ground terminal.

#### recommended operating conditions

|                                    | Sr  | V54LS1 | 45  | SN74LS145 |     |      |      |
|------------------------------------|-----|--------|-----|-----------|-----|------|------|
|                                    | MIN | NOM    | MAX | MIN       | NOM | MAX  | UNIT |
| Supply voltage, VCC                | 4.5 | 5      | 5.5 | 4.75      | 5   | 5.25 | v    |
| Off-state output voltage, VO(off)  |     |        | 15  |           |     | 15   | V    |
| Operating free-air temperature, TA | -55 |        | 125 | 0         |     | 70   | °C   |

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

|                    | PARAMETER                              | TEST CON   | DITIONE <sup>†</sup>                             | SN   | 154LS1 | 45   | S   | N74LS1       | 45   |    |
|--------------------|--|--|--|------|--------|------|-----|--------------|------|----|
|                    |  |  | MIN  | TYP‡ | MAX    | MIN  | TYч | MAX          |      |    |
| ⊻ін                | High-level input voltage               |  |  | 2    |        |      | 2   |              |      | V  |
| VIL                | Low-level input voltage                |  |  |      |        | 0.7  | †   |              | 0.8  | V  |
| VIK                | Input clamp voltage                    | V <sub>CC</sub> = MIN,   | lı = -18 mA                                      |      |        | -1.5 | -   |              | -1.5 | V  |
| IO(off)            | Off-state output current               | V <sub>CC</sub> ≠ MIN,<br>V <sub>IL</sub> = V <sub>IL</sub> max, | V <sub>IH</sub> = 2 V,<br>V <sub>OH</sub> = 15 V |      |        | 250  |     |              | 250  | μА |
| V <sub>O(on)</sub> | On-state output voltage                | V <sub>CC</sub> ∸ MIN,<br>V <sub>IH</sub> ≈ 2 V,                 | I <sub>OL</sub> = 12 mA                          | L    | 0,25   | 0.4  |     | 0.25<br>0.35 | 0.4  | v  |
|                    |  | $V_{1L} = V_{1L} \max$   | I <sub>OL</sub> = 80 mA                          |      |        |      |     | 2.3          | 3    | 1  |
| -ų                 | Input current at maximum input voltage | VCC = MAX,   | V <sub>1</sub> = 7 V                             |      |        | 0.1  |     |              | 0.1  | mA |
| Чн                 | High-level input current               | V <sub>CC</sub> = MAX,   | VI = 2.7 V                                       |      |        | 20   |     |              | 20   | μA |
| III.               | Law-level input current                | V <sub>CC</sub> = MAX,   | VI = 0.4 V                                       |      |        | -0.4 |     |              | -0.4 | mA |
| 'cc                | Supply current                         | V <sub>CC</sub> = MAX,   | See Note 2                                       |      | 7      | 13   |     | 7            | 13   | mA |

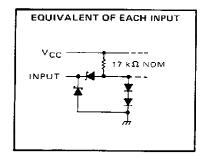
<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.  $\frac{1}{2}$ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. NOTE 2: 1<sub>CC</sub> is measured with all inputs grounded and outputs open.

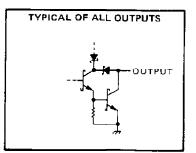
### switching characteristics, VCC = 5 V, TA = 25°C

| PARAMETER   |             | TEST CONDITI            | IONS       | MIN N | MAX | UNIT |
|---|-------------|-------------------------|------------|-------|-----|------|
| tPLH Propagation delay time, iow-to-high-level output | C) = 45 pF, | R <sub>1</sub> = 665 Ω. | See Note 3 |       | 50  | ns   |
| tPHL Propagation delay time, high-to-low-level output | сц - 45 рг, | ΗL = 665 Ω,             | Jee Note 5 |       | 50  | ns   |

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

### schematic of inputs and outputs







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4-Jun-2007

## **PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package<br>Type | Package<br>Drawing | Pins | Package<br>Qty | e Eco Plan <sup>(2)</sup> | Lead/Ball Finish | n MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|-----------------|--------------------|------|----------------|---------------------------|------------------|--------------------------------|
| 5962-8508401VEA  | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | Call TI          | Call TI                        |
| 85084012A        | ACTIVE                | LCCC            | FK                 | 20   | 1              | TBD                       | POST-PLATE       | N / A for Pkg Type             |
| 85084012A        | ACTIVE                | LCCC            | FK                 | 20   | 1              | TBD                       | POST-PLATE       | N / A for Pkg Type             |
| 8508401EA        | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| 8508401EA        | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| 8508401FA        | ACTIVE                | CFP             | W                  | 16   | 1              | TBD                       | A42              | N / A for Pkg Type             |
| 8508401FA        | ACTIVE                | CFP             | W                  | 16   | 1              | TBD                       | A42              | N / A for Pkg Type             |
| SN54LS145J       | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| SN54LS145J       | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| SN74145N         | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145N         | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145N3        | OBSOLETE              | PDIP            | Ν                  | 16   |                | TBD                       | Call TI          | Call TI                        |
| SN74145N3        | OBSOLETE              | PDIP            | Ν                  | 16   |                | TBD                       | Call TI          | Call TI                        |
| SN74145NE4       | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145NE4       | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145NSR       | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSR       | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRE4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRE4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRG4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRG4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145D       | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145D       | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DE4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DE4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DG4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DG4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DR      | ACTIVE                | SOIC            | D                  | 16   | 2500           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
|                  | ACTIVE                | SOIC            | D                  | 16   | 2500           | Green (RoHS &             | CU NIPDAU        | Level-1-260C-UNLIM             |

## PACKAGE OPTION ADDENDUM

4-Jun-2007

| Orderable Device | Status <sup>(1)</sup> | Package<br>Type | Package<br>Drawing | Pins | Packag<br>Qty | e Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|-----------------|--------------------|------|---------------|---------------------------|------------------|------------------------------|
| SN74LS145DRE4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145DRE4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145DRG4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145DRG4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145N       | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type           |
| SN74LS145N       | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type           |
| SN74LS145N3      | OBSOLETE              | PDIP            | Ν                  | 16   |               | TBD                       | Call TI          | Call TI                      |
| SN74LS145N3      | OBSOLETE              | PDIP            | Ν                  | 16   |               | TBD                       | Call TI          | Call TI                      |
| SN74LS145NE4     | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type           |
| SN74LS145NE4     | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type           |
| SN74LS145NSR     | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145NSR     | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145NSRE4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145NSRE4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145NSRG4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LS145NSRG4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM           |
| SNJ54145J        | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type           |
| SNJ54145J        | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type           |
| SNJ54LS145FK     | ACTIVE                | LCCC            | FK                 | 20   | 1             | TBD                       | POST-PLATE       | N / A for Pkg Type           |
| SNJ54LS145FK     | ACTIVE                | LCCC            | FK                 | 20   | 1             | TBD                       | POST-PLATE       | N / A for Pkg Type           |
| SNJ54LS145J      | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type           |
| SNJ54LS145J      | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type           |
| SNJ54LS145W      | ACTIVE                | CFP             | W                  | 16   | 1             | TBD                       | A42              | N / A for Pkg Type           |
| SNJ54LS145W      | ACTIVE                | CFP             | W                  | 16   | 1             | TBD                       | A42              | N / A for Pkg Type           |

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered



at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

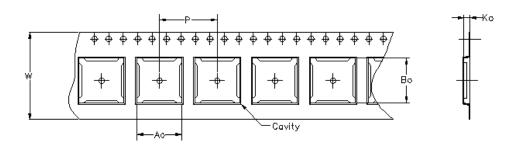
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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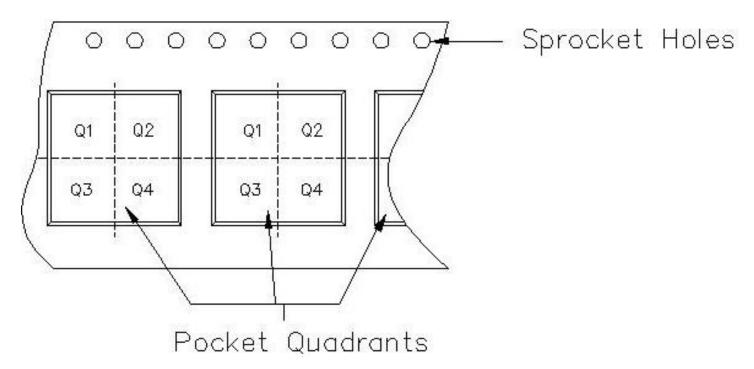


9-Jun-2007



Carrier tape design is defined largely by the component lentgh, width, and thickness.

| Ao = Dimension designed to accommodate the component width.     |
|---|
| Bo = Dimension designed to accommodate the component length.    |
| Ko = Dimension designed to accommodate the component thickness. |
| W = Overall width of the carrier tape.                          |
| P = Pitch between successive cavity centers.                    |



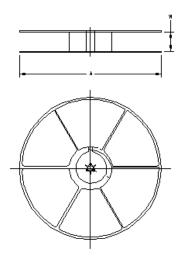
TAPE AND REEL INFORMATION

# PACKAGE MATERIALS INFORMATION



9-Jun-2007

| Device       | Package | Pins | Site | Reel<br>Diameter<br>(mm) | Reel<br>Width<br>(mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1<br>(mm) | W<br>(mm) | Pin1<br>Quadrant |
|--------------|---------|------|------|--------------------------|-----------------------|---------|---------|---------|------------|-----------|------------------|
| SN74145NSR   | NS      | 16   | MLA  | 330                      | 16                    | 8.2     | 10.5    | 2.5     | 12         | 16        | Q1               |
| SN74LS145DR  | D       | 16   | FMX  | 330                      | 16                    | 6.5     | 10.3    | 2.1     | 8          | 16        | Q1               |
| SN74LS145NSR | NS      | 16   | MLA  | 330                      | 16                    | 8.2     | 10.5    | 2.5     | 12         | 16        | Q1               |



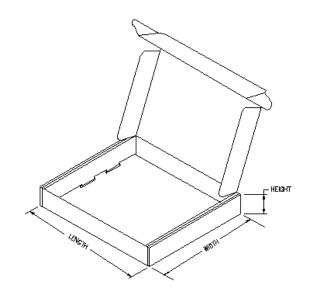
## TAPE AND REEL BOX INFORMATION

| Device       | Package | Pins | Site | Length (mm) | Width (mm) | Height (mm) |
|--------------|---------|------|------|-------------|------------|-------------|
| SN74145NSR   | NS      | 16   | MLA  | 342.9       | 336.6      | 28.58       |
| SN74LS145DR  | D       | 16   | FMX  | 342.9       | 336.6      | 28.58       |
| SN74LS145NSR | NS      | 16   | MLA  | 342.9       | 336.6      | 28.58       |



# PACKAGE MATERIALS INFORMATION

9-Jun-2007



J (R-GDIP-T\*\*) 14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package can be hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only.
  - E. Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC



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## **PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package<br>Type | Package<br>Drawing | Pins | Package<br>Qty | e Eco Plan <sup>(2)</sup> | Lead/Ball Finish | n MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|-----------------|--------------------|------|----------------|---------------------------|------------------|--------------------------------|
| 5962-8508401VEA  | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| 85084012A        | ACTIVE                | LCCC            | FK                 | 20   | 1              | TBD                       | POST-PLATE       | N / A for Pkg Type             |
| 85084012A        | ACTIVE                | LCCC            | FK                 | 20   | 1              | TBD                       | POST-PLATE       | N / A for Pkg Type             |
| 8508401EA        | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| 8508401EA        | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| 8508401FA        | ACTIVE                | CFP             | W                  | 16   | 1              | TBD                       | A42              | N / A for Pkg Type             |
| 8508401FA        | ACTIVE                | CFP             | W                  | 16   | 1              | TBD                       | A42              | N / A for Pkg Type             |
| SN54LS145J       | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| SN54LS145J       | ACTIVE                | CDIP            | J                  | 16   | 1              | TBD                       | A42 SNPB         | N / A for Pkg Type             |
| SN74145N         | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145N         | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145N3        | OBSOLETE              | PDIP            | Ν                  | 16   |                | TBD                       | Call TI          | Call TI                        |
| SN74145N3        | OBSOLETE              | PDIP            | Ν                  | 16   |                | TBD                       | Call TI          | Call TI                        |
| SN74145NE4       | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145NE4       | ACTIVE                | PDIP            | Ν                  | 16   | 25             | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type             |
| SN74145NSR       | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSR       | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRE4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRE4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRG4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74145NSRG4     | ACTIVE                | SO              | NS                 | 16   | 2000           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145D       | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145D       | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DE4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DE4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DG4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DG4     | ACTIVE                | SOIC            | D                  | 16   | 40             | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
| SN74LS145DR      | ACTIVE                | SOIC            | D                  | 16   | 2500           | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM             |
|                  | ACTIVE                | SOIC            | D                  | 16   | 2500           | Green (RoHS &             | CU NIPDAU        | Level-1-260C-UNLIM             |

## PACKAGE OPTION ADDENDUM

9-Oct-2007

| Orderable Device | Status <sup>(1)</sup> | Package<br>Type | Package<br>Drawing | Pins | Packag<br>Qty | e Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3</sup> |
|------------------|-----------------------|-----------------|--------------------|------|---------------|---------------------------|------------------|-----------------------------|
| SN74LS145DRE4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145DRE4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145DRG4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145DRG4    | ACTIVE                | SOIC            | D                  | 16   | 2500          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145N       | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type          |
| SN74LS145N       | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type          |
| SN74LS145N3      | OBSOLETE              | PDIP            | Ν                  | 16   |               | TBD                       | Call TI          | Call TI                     |
| SN74LS145N3      | OBSOLETE              | PDIP            | Ν                  | 16   |               | TBD                       | Call TI          | Call TI                     |
| SN74LS145NE4     | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type          |
| SN74LS145NE4     | ACTIVE                | PDIP            | Ν                  | 16   | 25            | Pb-Free<br>(RoHS)         | CU NIPDAU        | N / A for Pkg Type          |
| SN74LS145NSR     | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145NSR     | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIM          |
| SN74LS145NSRE4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIN          |
| SN74LS145NSRE4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIN          |
| SN74LS145NSRG4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIN          |
| SN74LS145NSRG4   | ACTIVE                | SO              | NS                 | 16   | 2000          | Green (RoHS & no Sb/Br)   | CU NIPDAU        | Level-1-260C-UNLIN          |
| SNJ54145J        | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type          |
| SNJ54145J        | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type          |
| SNJ54LS145FK     | ACTIVE                | LCCC            | FK                 | 20   | 1             | TBD                       | POST-PLATE       | N / A for Pkg Type          |
| SNJ54LS145FK     | ACTIVE                | LCCC            | FK                 | 20   | 1             | TBD                       | POST-PLATE       | N / A for Pkg Type          |
| SNJ54LS145J      | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type          |
| SNJ54LS145J      | ACTIVE                | CDIP            | J                  | 16   | 1             | TBD                       | A42 SNPB         | N / A for Pkg Type          |
| SNJ54LS145W      | ACTIVE                | CFP             | W                  | 16   | 1             | TBD                       | A42              | N / A for Pkg Type          |
| SNJ54LS145W      | ACTIVE                | CFP             | W                  | 16   | 1             | TBD                       | A42              | N / A for Pkg Type          |

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered



at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

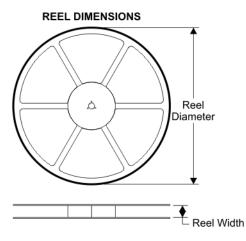
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

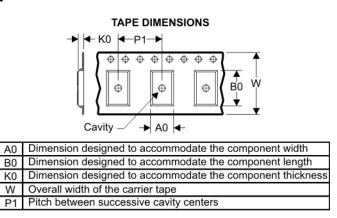
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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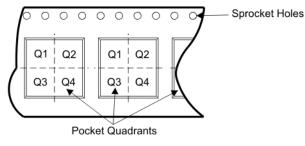
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## TAPE AND REEL BOX INFORMATION





### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

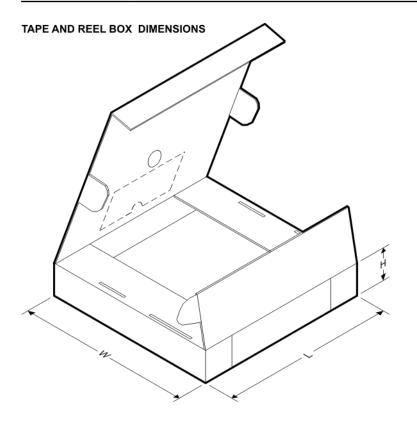


| Device       | Package | Pins | Site    | Reel<br>Diameter<br>(mm) | Reel<br>Width<br>(mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1<br>(mm) | W<br>(mm) | Pin1<br>Quadrant |
|--------------|---------|------|---------|--------------------------|-----------------------|---------|---------|---------|------------|-----------|------------------|
| SN74145NSR   | NS      | 16   | SITE 41 | 330                      | 16                    | 8.2     | 10.5    | 2.5     | 12         | 16        | Q1               |
| SN74LS145DR  | D       | 16   | SITE 27 | 330                      | 16                    | 6.5     | 10.3    | 2.1     | 8          | 16        | Q1               |
| SN74LS145NSR | NS      | 16   | SITE 41 | 330                      | 16                    | 8.2     | 10.5    | 2.5     | 12         | 16        | Q1               |



# PACKAGE MATERIALS INFORMATION

4-Oct-2007



| Device       | Package | Pins | Site    | Length (mm) | Width (mm) | Height (mm) |
|--------------|---------|------|---------|-------------|------------|-------------|
| SN74145NSR   | NS      | 16   | SITE 41 | 346.0       | 346.0      | 33.0        |
| SN74LS145DR  | D       | 16   | SITE 27 | 342.9       | 336.6      | 28.58       |
| SN74LS145NSR | NS      | 16   | SITE 41 | 346.0       | 346.0      | 33.0        |

J (R-GDIP-T\*\*) 14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package can be hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only.
  - E. Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC

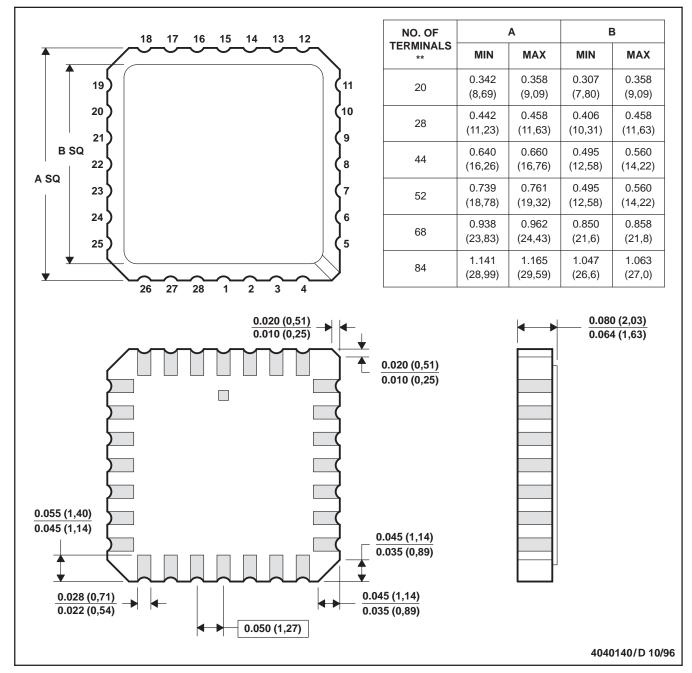


MLCC006B - OCTOBER 1996

## FK (S-CQCC-N\*\*)

### LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004



## N (R-PDIP-T\*\*)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



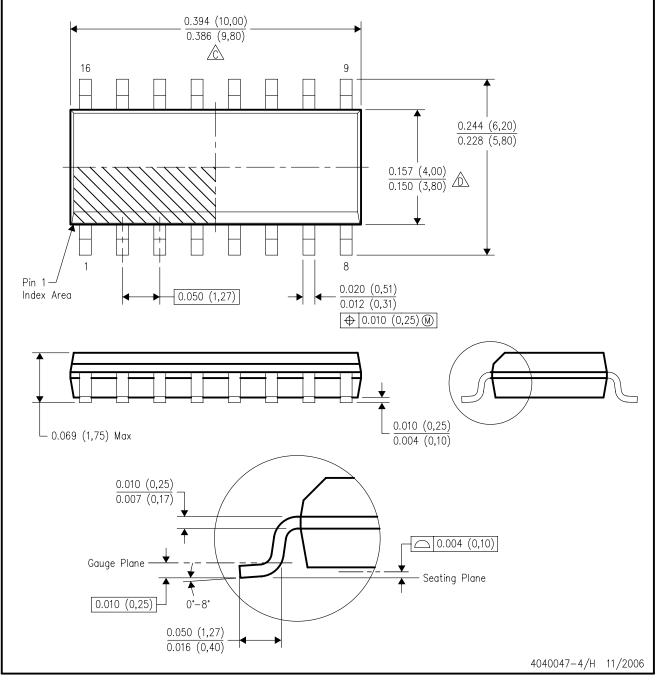
NOTES:

- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- $\triangle$  The 20 pin end lead shoulder width is a vendor option, either half or full width.



D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.

E. Reference JEDEC MS-012 variation AC.



## MECHANICAL DATA

## PLASTIC SMALL-OUTLINE PACKAGE

### 0,51 0,35 ⊕0,25⊛ 1,27 8 14 0,15 NOM 5,60 8,20 5,00 7,40 $\bigcirc$ Gage Plane ₽ 0,25 7 1 1,05 0,55 0-10 Δ 0,15 0,05 Seating Plane — 2,00 MAX 0,10PINS \*\* 14 16 20 24 DIM 10,50 10,50 12,90 15,30 A MAX A MIN 9,90 9,90 12,30 14,70 4040062/C 03/03

NOTES: A. All linear dimensions are in millimeters.

NS (R-PDSO-G\*\*)

**14-PINS SHOWN** 

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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| Power Mgmt            | power.ti.com           | Optical Networking | www.ti.com/opticalnetwork |
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| RFID                  | www.ti-rfid.com        | Telephony          | www.ti.com/telephony      |
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|                       |                        | Wireless           | www.ti.com/wireless       |

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